

High Speed Connector 0.9mm Pitch Systems



■ Features

- Higher bandwidth applications 56 Gbps NRZ, 112 Gbps PAM-4
- Stack heights from 5.0mm
- High pin counts : 120 total contacts(25 differential pairs)
- Pitch 0.9 mm x 1.5mm
- Differential pair 92ohm nominal impedance
- Surface Mount BGA Pin Design

■ Benefits

- Minimizes impedance discontinuities
- Excellent Insertion and Return loss performance
- Low crosstalk noise and resonances
- Biggest forced-offset(Rigid alignment) tolerance
- Hermaphroditic mating interface
- Integrated power and additional signal pin per column

■ Application

- Telecommunication and Data Embedded
- Data Servers and Storage
- Industrial Controls and Equipment
- Medical Instrumentation
- Military Electronics
- Network Diagnostics
- Test and Measurement Electronics

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■ MECHANICAL PERFORMANCE

- Mating Tolerance :
 - X: $\pm 1.0\text{mm}$ (floating mating)
 - Y: $\pm 1.2\text{mm}$ (floating mating)
 - Z: $-0.2/+0.5\text{mm}$
- Forced-offset (Rigid alignment) tolerance : X/Y $\pm 0.2\text{mm}$
- Mating Force 0.45N max. per contact
- Un-mating Force 0.1N min. per contact

■ ELECTRICAL PERFORMANCE

- Contact Resistance: $<10\text{m}\Omega$ change from initial reading after environmental exposure
- Current Rating(with $<30^\circ\text{C}$ temperature rise above ambient): 0.5A min
- Insulation Resistance : 1000Mohm min
- Withstanding Voltage : 500Vrms min

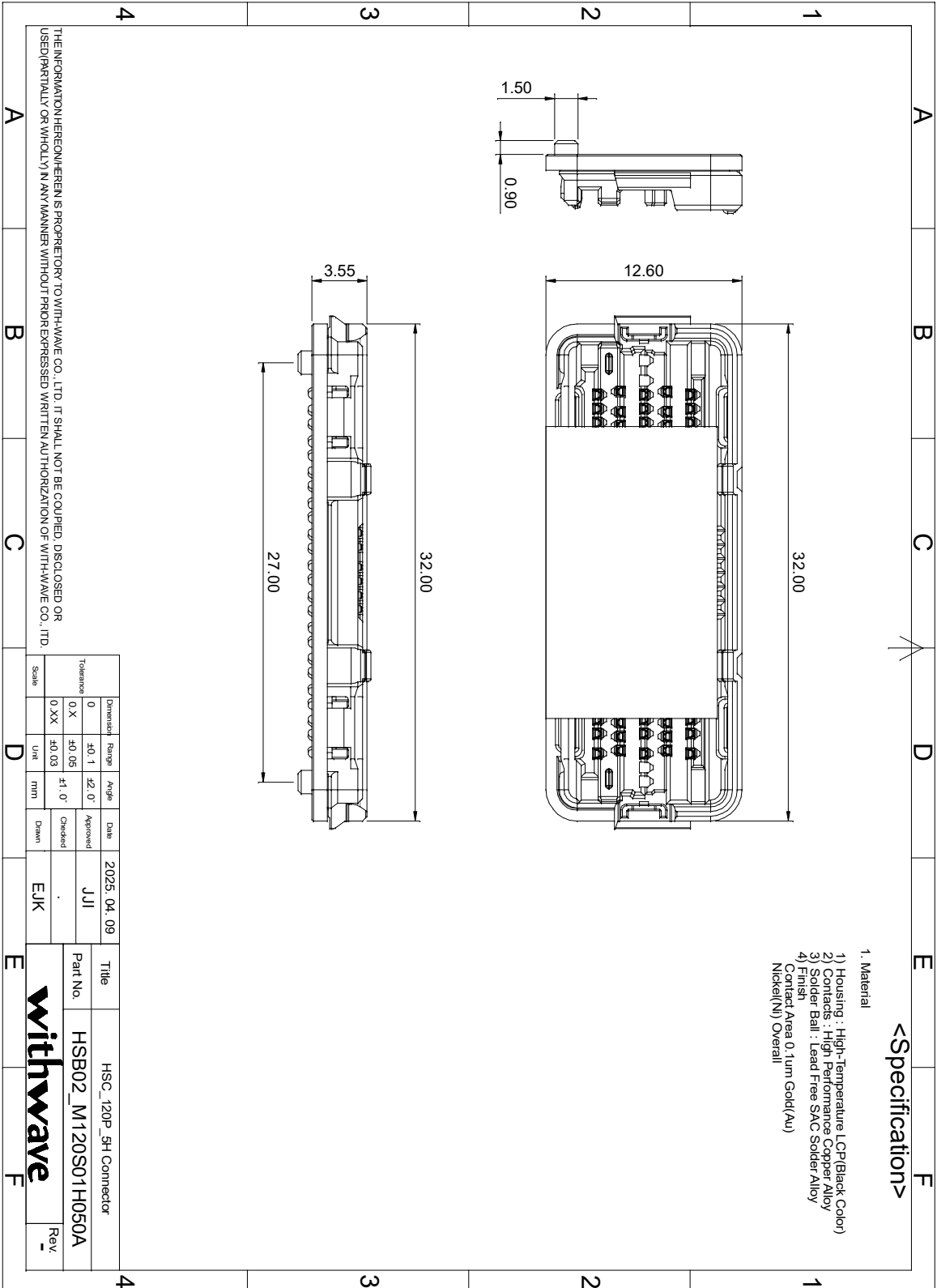
■ MATERIAL

- Housing : High-Temperature LCP, UL94V-01
- Contacts : High performance Copper Alloy
- Plating(s) :
 - Contact Area 0.1 μm Gold(Au)
 - Nickel(Ni) Overall

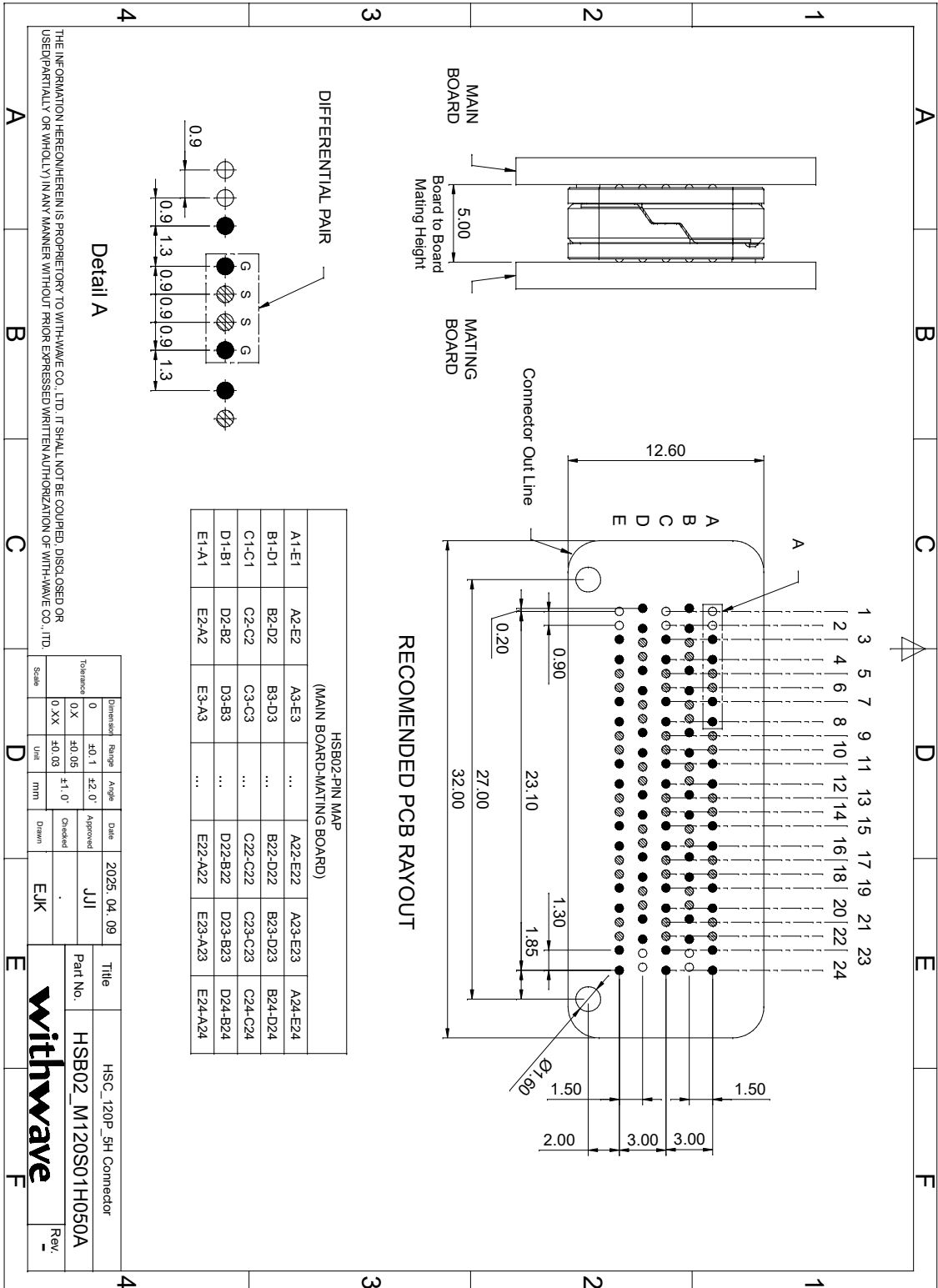
■ ENVIRONMENTAL

- Operating Temperature range : -55 to $+85^\circ\text{C}$

CONNECTOR DRAWING



RECOMMENDED PCB LAYOUT (BOARD TO BOARD)



PCB DESIGN GUIDE (Applied to high speed systems)

• PCB Stack up

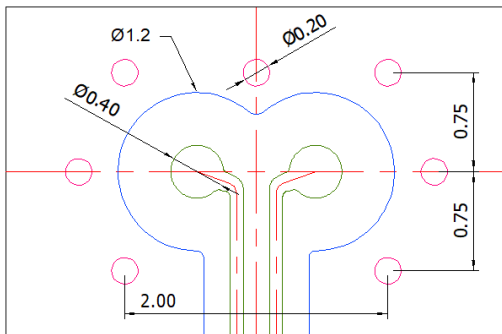
Layer	Thickness
1	Copper (1oz) 0.045
	Dielectric 01 0.06
2	Copper (1oz) 0.034
	Dielectric 02 0.3
3	Copper (1oz) 0.045

← Signal Trace

- Dielectric 01 : P.P 1080 Er 3.9
- Dielectric 02 : FR4 Er 4.4

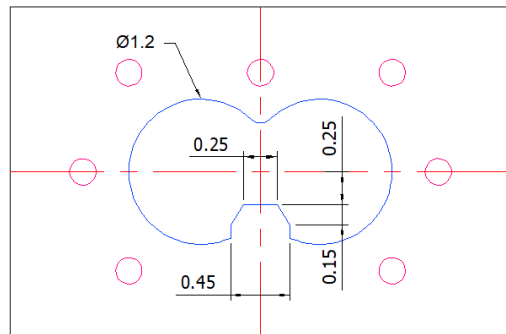
• PCB Layer1 Transition PAD Cutout Thru Via(Ø0.2)

Layer1

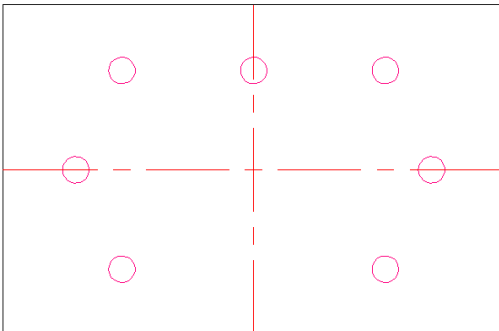


Differential Line

Layer2

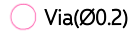


Layer3

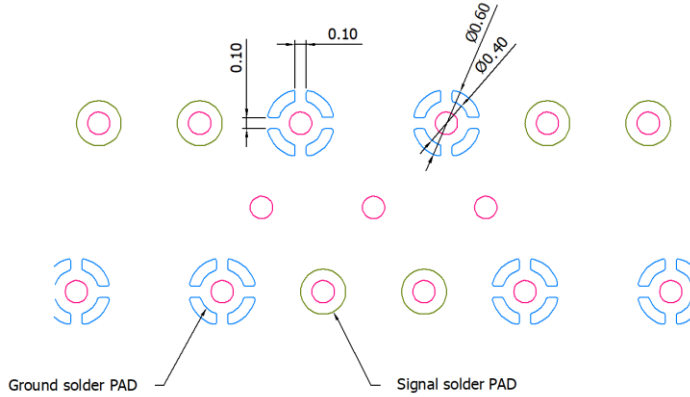


- If the thickness of dielectric 02 is less than 0.3 , copper cut out is required in the layer3

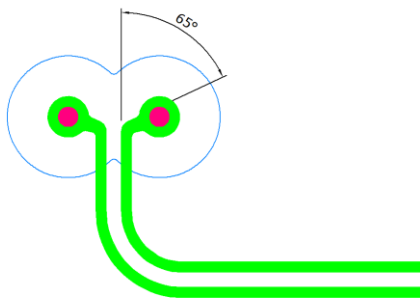
PCB DESIGN GUIDE



Thermal Relief Pad

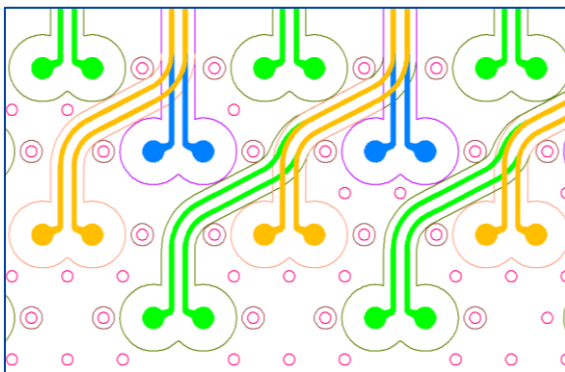


Differential Pair Signal PAD

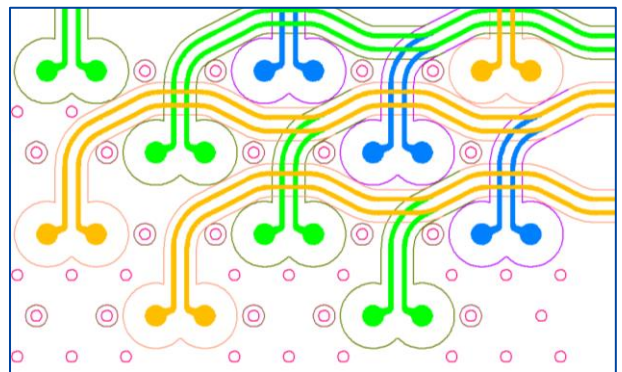


Differential Pair Routing

- Recommended to route the PCB traces one pitch away.
- Add a GND via between signal and signal for maximum cross talk performance.



Vertical routing



Horizontal routing